

IRF7433PbF

HEXFET[®] Power MOSFET

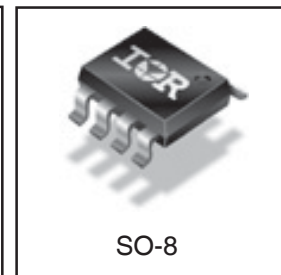
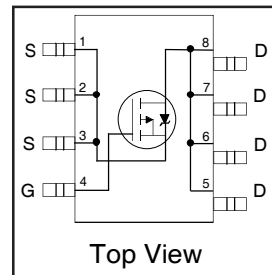
- Ultra Low On-Resistance
- P-Channel MOSFET
- Surface Mount
- Available in Tape & Reel
- Lead-Free

V_{DS}	$R_{DS(on)}$ max	I_D
-12V	24m Ω @ $V_{GS} = -4.5V$	-8.7A
	30m Ω @ $V_{GS} = -2.5V$	-7.4A
	46m Ω @ $V_{GS} = -1.8V$	-6.3A

Description

These P-Channel MOSFETs from International Rectifier utilize advanced processing techniques to achieve the extremely low on-resistance per silicon area. This benefit provides the designer with an extremely efficient device for use in battery and load management applications..

The SO-8 has been modified through a customized leadframe for enhanced thermal characteristics and multiple-die capability making it ideal in a variety of power applications. With these improvements, multiple devices can be used in an application with dramatically reduced board space. The package is designed for vapor phase, infrared, or wave soldering techniques.



Absolute Maximum Ratings

	Parameter	Max.	Units
V_{DS}	Drain-Source Voltage	-12	V
I_D @ $T_A = 25^\circ C$	Continuous Drain Current, V_{GS} @ -4.5V	-8.9	A
I_D @ $T_A = 70^\circ C$	Continuous Drain Current, V_{GS} @ -4.5V	-7.1	
I_{DM}	Pulsed Drain Current ^①	-36	
P_D @ $T_A = 25^\circ C$	Maximum Power Dissipation ^③	2.5	W
P_D @ $T_A = 70^\circ C$	Maximum Power Dissipation ^③	1.6	W
	Linear Derating Factor	0.02	W/ $^\circ C$
V_{GS}	Gate-to-Source Voltage	± 8	V
T_J, T_{STG}	Junction and Storage Temperature Range	-55 to +150	$^\circ C$

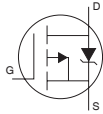
Thermal Resistance

	Parameter	Max.	Units
$R_{\theta JA}$	Maximum Junction-to-Ambient ^③	50	$^\circ C/W$

Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
$V_{(BR)DSS}$	Drain-to-Source Breakdown Voltage	-12	—	—	V	$V_{GS} = 0V, I_D = -250\mu A$
$\Delta V_{(BR)DSS}/\Delta T_J$	Breakdown Voltage Temp. Coefficient	—	0.007	—	V/°C	Reference to 25°C , $I_D = -1\text{mA}$
$R_{DS(on)}$	Static Drain-to-Source On-Resistance	—	—	24	m Ω	$V_{GS} = -4.5V, I_D = -8.7A$ ②
		—	—	30		$V_{GS} = -2.5V, I_D = -7.4A$ ②
		—	—	46		$V_{GS} = -1.8V, I_D = -6.3A$ ②
$V_{GS(th)}$	Gate Threshold Voltage	-0.4	—	-0.9	V	$V_{DS} = V_{GS}, I_D = -250\mu A$
g_{fs}	Forward Transconductance	22	—	—	S	$V_{DS} = -10V, I_D = -8.7A$
I_{DSS}	Drain-to-Source Leakage Current	—	—	-1.0	μA	$V_{DS} = -9.6V, V_{GS} = 0V$
		—	—	-25		$V_{DS} = -9.6V, V_{GS} = 0V, T_J = 70^\circ\text{C}$
I_{GSS}	Gate-to-Source Forward Leakage	—	—	-100	nA	$V_{GS} = -8V$
	Gate-to-Source Reverse Leakage	—	—	100		$V_{GS} = 8V$
Q_g	Total Gate Charge	—	20	—	nC	$I_D = -8.7A$
Q_{gs}	Gate-to-Source Charge	—	4.5	—		$V_{DS} = -6V$
Q_{gd}	Gate-to-Drain ("Miller") Charge	—	4.0	—		$V_{GS} = -4.5V$ ②
$t_{d(on)}$	Turn-On Delay Time	—	8.8	13	ns	$V_{DD} = -6V, V_{GS} = -4.5V$
t_r	Rise Time	—	8.2	12		$I_D = -1.0A$
$t_{d(off)}$	Turn-Off Delay Time	—	272	408		$R_D = 6\Omega$
t_f	Fall Time	—	175	263		$R_G = 6\Omega$ ②
C_{iss}	Input Capacitance	—	1877	—		$V_{GS} = 0V$
C_{oss}	Output Capacitance	—	512	—	pF	$V_{DS} = -10V$
C_{rss}	Reverse Transfer Capacitance	—	310	—		$f = 1.0\text{MHz}$

Source-Drain Ratings and Characteristics

	Parameter	Min.	Typ.	Max.	Units	Conditions
I_S	Continuous Source Current (Body Diode)	—	—	-2.5	A	MOSFET symbol showing the integral reverse p-n junction diode. 
I_{SM}	Pulsed Source Current (Body Diode) ①	—	—	-36		
V_{SD}	Diode Forward Voltage	—	—	-1.2	V	$T_J = 25^\circ\text{C}, I_S = -2.5A, V_{GS} = 0V$ ②
t_{rr}	Reverse Recovery Time	—	36	54	ns	$T_J = 25^\circ\text{C}, I_F = -2.5A$
Q_{rr}	Reverse Recovery Charge	—	28	42	nC	$di/dt = -100A/\mu s$ ②

Notes:

① Repetitive rating; pulse width limited by max. junction temperature.

② Pulse width $\leq 400\mu s$; duty cycle $\leq 2\%$.

③ When mounted on 1 inch square copper board, $t < 10$ sec.

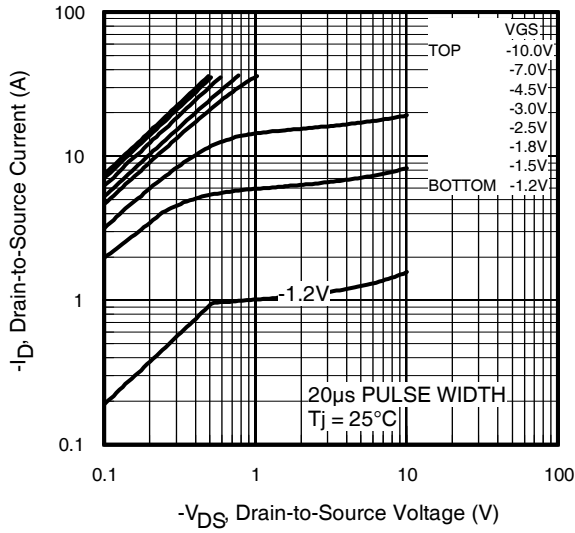


Fig 1. Typical Output Characteristics

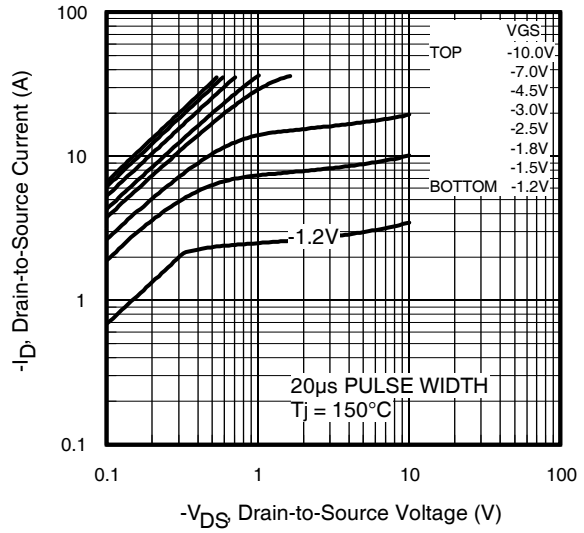


Fig 2. Typical Output Characteristics

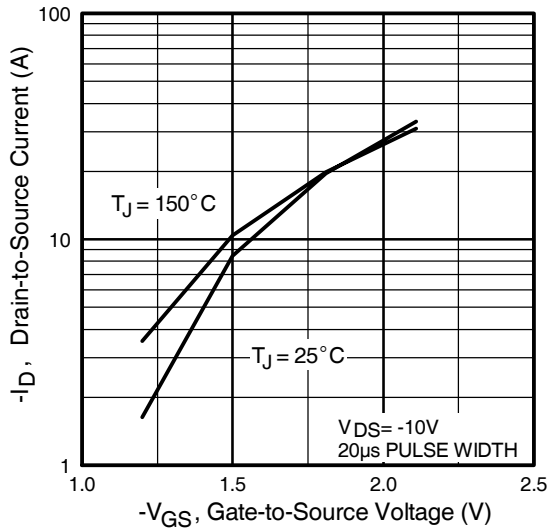


Fig 3. Typical Transfer Characteristics

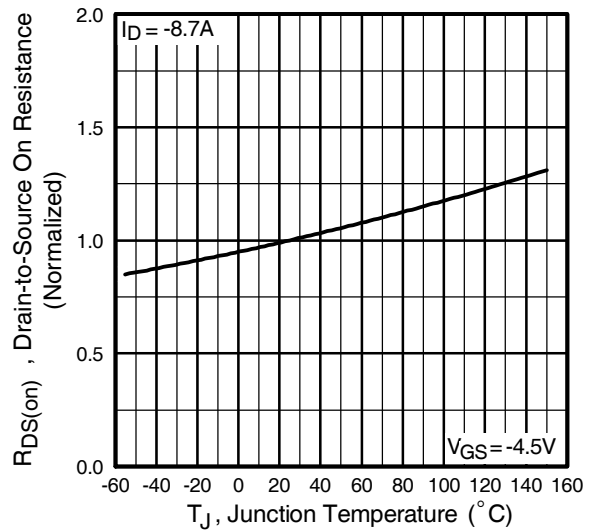


Fig 4. Normalized On-Resistance Vs. Temperature

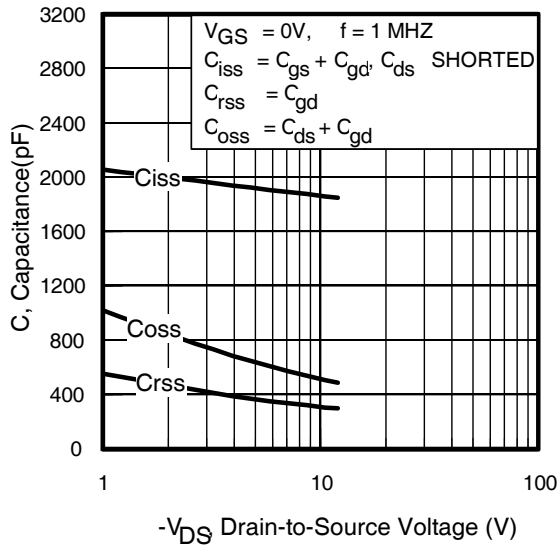


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

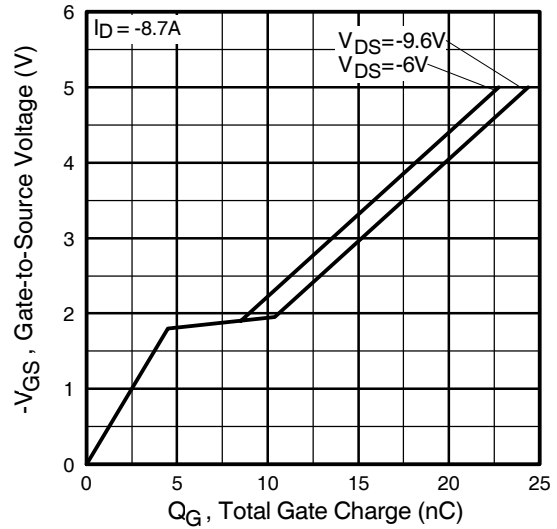


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

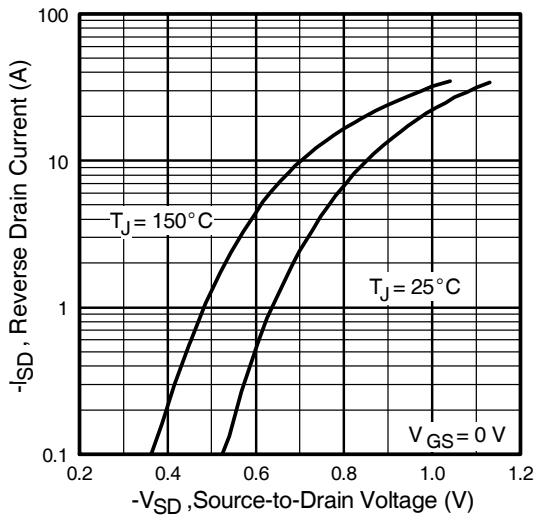


Fig 7. Typical Source-Drain Diode Forward Voltage

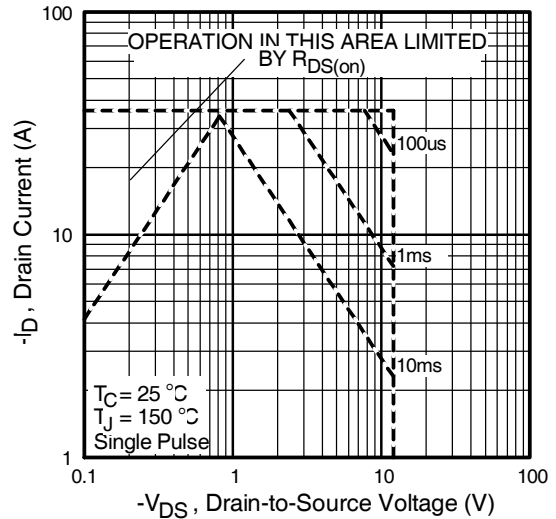


Fig 8. Maximum Safe Operating Area

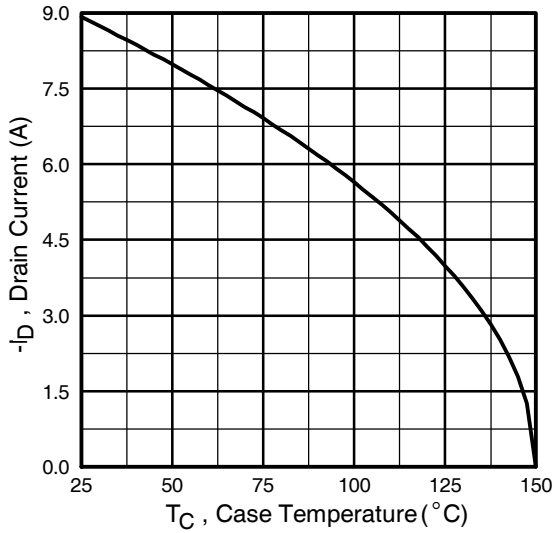


Fig 9. Maximum Drain Current Vs. Case Temperature

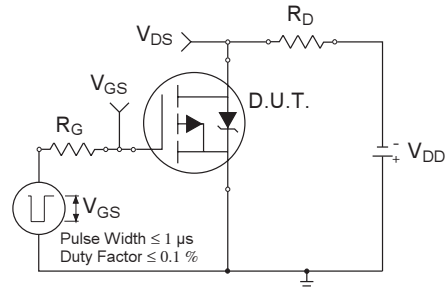


Fig 10a. Switching Time Test Circuit

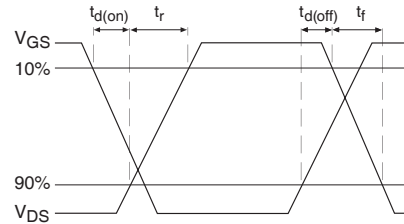


Fig 10b. Switching Time Waveforms

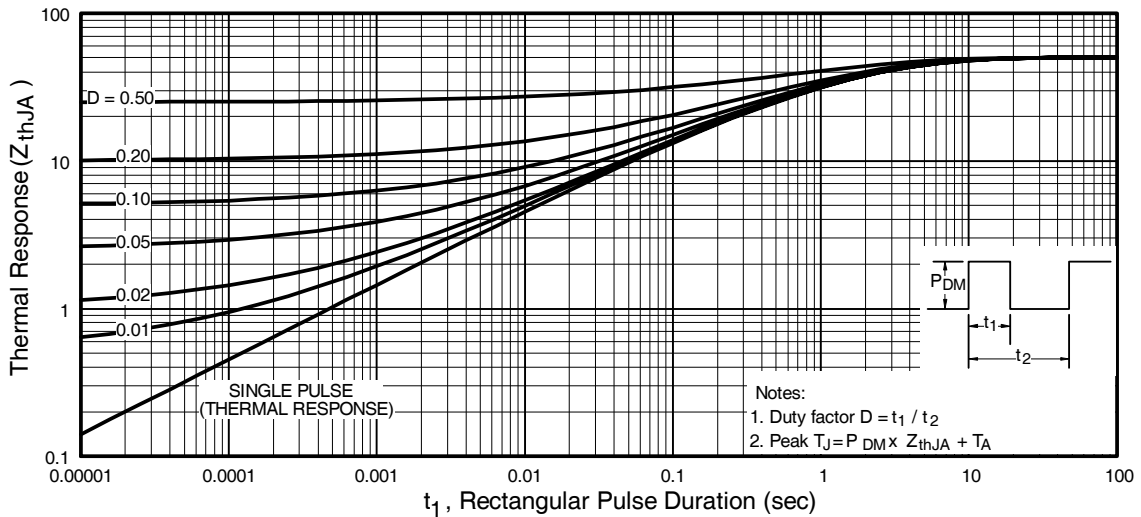


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

IRF7433PbF

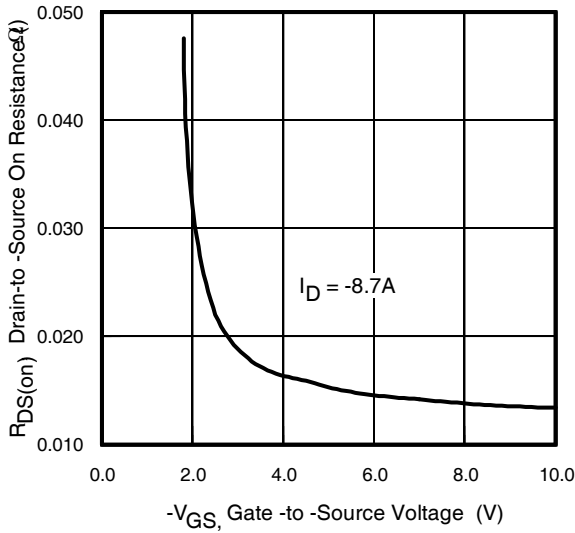


Fig 12. Typical On-Resistance Vs. Gate Voltage

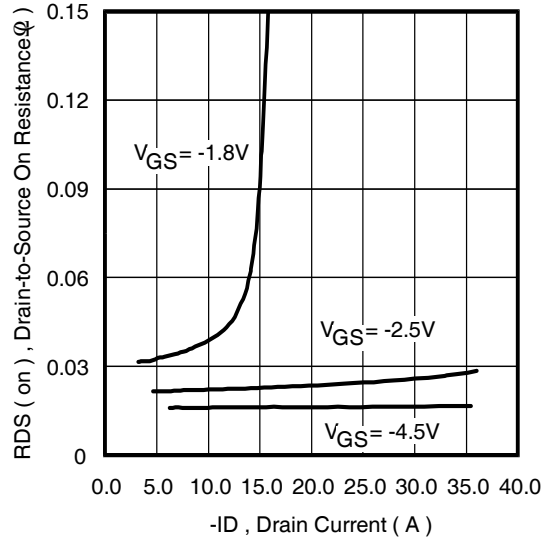


Fig 13. Typical On-Resistance Vs. Drain Current

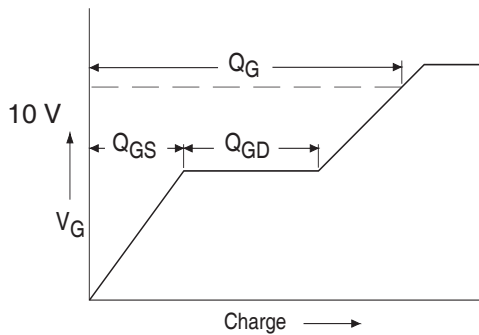


Fig 14a. Basic Gate Charge Waveform

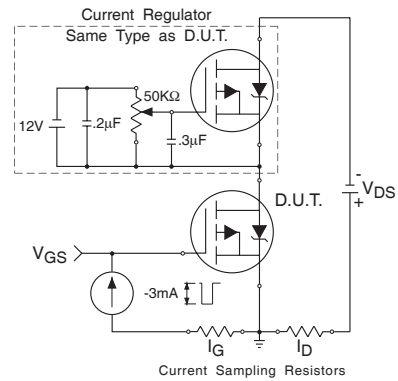


Fig 14b. Gate Charge Test Circuit

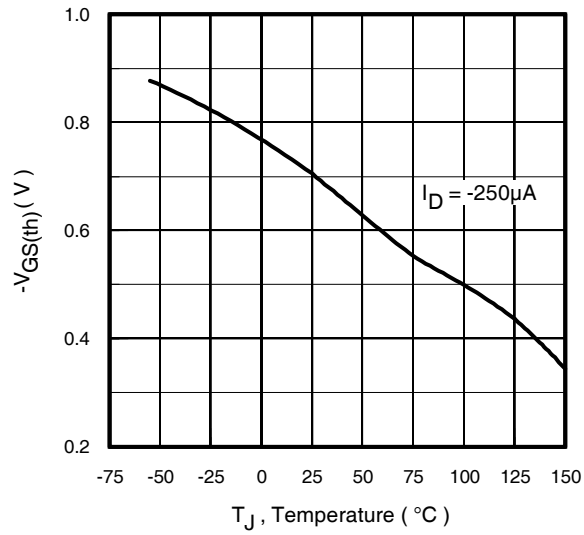


Fig 15. Typical $V_{GS(th)}$ Vs. Junction Temperature

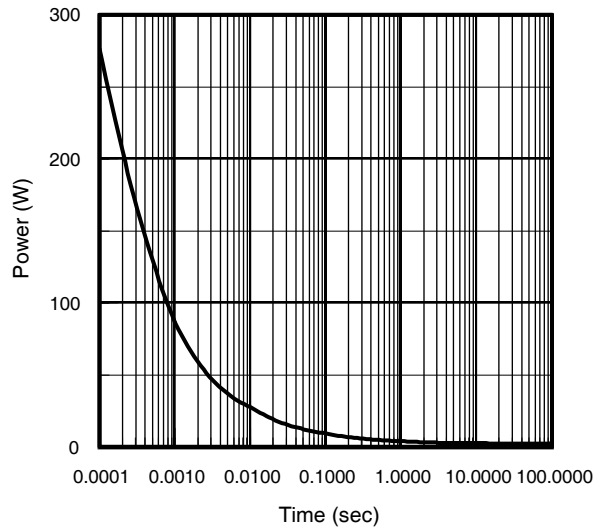
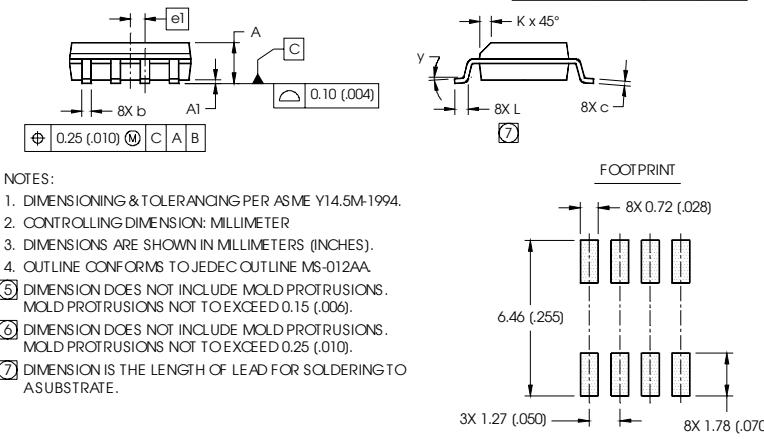
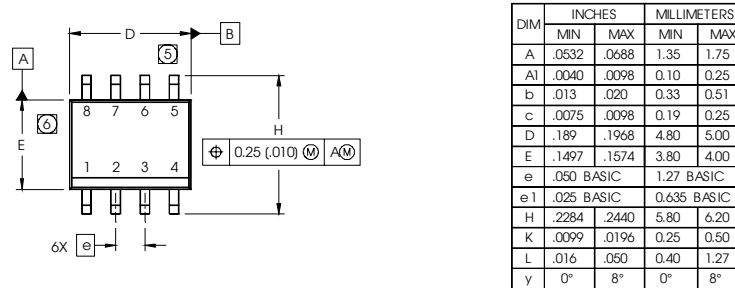


Fig 16. Typical Power Vs. Time

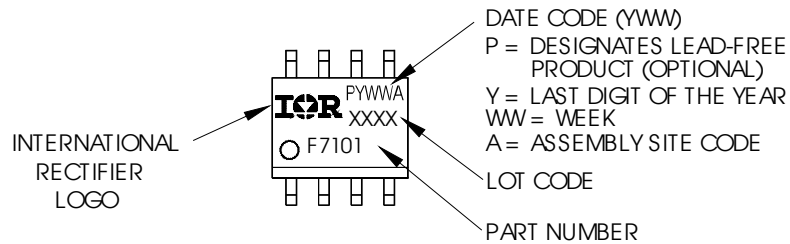
SO-8 Package Outline

Dimensions are shown in millimeters (inches)



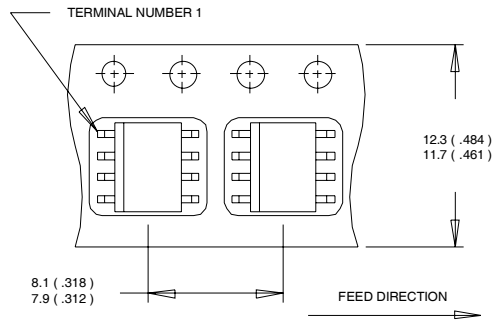
SO-8 Part Marking Information (Lead-Free)

EXAMPLE: THIS IS AN IRF7101 (MOSFET)



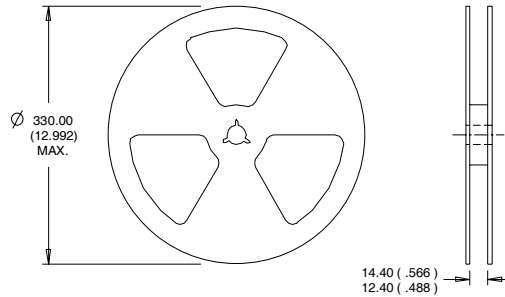
SO-8 Tape and Reel

Dimensions are shown in millimeters (inches)



NOTES:

1. CONTROLLING DIMENSION : MILLIMETER.
2. ALL DIMENSIONS ARE SHOWN IN MILLIMETERS(INCHES).
3. OUTLINE CONFORMS TO EIA-481 & EIA-541.



NOTES:

1. CONTROLLING DIMENSION : MILLIMETER.
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Data and specifications subject to change without notice.
 This product has been designed and qualified for the Consumer market.
 Qualification Standards can be found on IR's Web site.