


PCN Number:	20170927000	PCN Date:	Oct. 17, 2017
Title:	Datasheet MSP430FR2311, MSP430FR2310		
Customer Contact:	PCN Manager	Dept:	Quality Services
Change Type:			
<input type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Design
<input type="checkbox"/>	Assembly Process	<input checked="" type="checkbox"/>	Data Sheet
<input type="checkbox"/>	Assembly Materials	<input type="checkbox"/>	Part number change
<input type="checkbox"/>	Mechanical Specification	<input type="checkbox"/>	Test Site
<input type="checkbox"/>	Packing/Shipping/Labeling	<input type="checkbox"/>	Test Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Material
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Materials
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Process
Notification Details			
Description of Change:			
<p>Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is being updated as summarized below. The following change history provides further details.</p>			
		MSP430FR2311, MSP430FR2310 SLASE58C –FEBRUARY 2016–REVISED SEPTEMBER 2017	
Changes from June 1, 2016 to September 11, 2017			
			Page
<ul style="list-style-type: none"> • Corrected the current in the "Shutdown (LPM4.5)" Features list item 1 • Low-leakage improved from 50pA to 5pA based on test data 1 • Added Section 3.1, Related Products 6 • Removed ADCDIV from the formula for the TYP value in the second row of the t_{CONVERT} parameter in Table 5-21, ADC, 10-Bit Timing Parameters (removed because ADCCLK is after division)..... 35 • Changed the entries for eUSCI_A0 and eUSCI_B0 in the LPM3 column from Off to Optional in Table 6-1, Operating Modes 44 • Added the sentence that begins "This device supports blank device detection..." in Section 6.6, Bootloader (BSL).. 46 • Added the note "Controlled by the RTCCLK bit in the SYSCFG2 register" on Table 6-8, Clock Distribution 49 • Added Figure 6-1, Clock Distribution Block Diagram 49 • Added Figure 6-2, Timer_B Connections 53 • Removed SYSBERRIV register (not supported) in Table 6-26, SYS Registers 62 • Changed from "If the RST/NMI pin is unused...with a 2.2-nF pulldown capacitor" to "If the RST/NMI pin is unused...with a 10-nF pulldown capacitor"..... 74 			
The datasheet number will be changing.			
Device Family	Change From:	Change To:	
TPS65218	SLASE58B	SLASE58C	
These changes may be reviewed at the datasheet links provided.			
http://www.ti.com/product/MSP430FR2311			
Reason for Change:			
To accurately reflect device characteristics.			
Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):			
No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.			
Changes to product identification resulting from this PCN:			
None.			
Product Affected:			
MSP430FR2310IPW16	MSP430FR2310IPW16R	MSP430FR2310IPW20	MSP430FR2310IPW20R
MSP430FR2310IRGYR	MSP430FR2310IRGYT	MSP430FR2311IPW16	MSP430FR2311IPW16R
MSP430FR2311IPW20	MSP430FR2311IPW20R	MSP430FR2311IRGYR	MSP430FR2311IRGYT

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

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