

Notification Number:	20221001002.0	Notification Date:	October 04, 2022
Title:	Datasheet for BQ79616-Q1, BQ79614-Q1, BQ79612-Q1		
Customer Contact:	Notification Manager	Dept:	Quality Services
Change Type: Electrical Specification			

Description of Change:

Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is being updated as summarized below. The following change history provides further details.



BQ79616-Q1, BQ79614-Q1, BQ79612-Q1
SLUSE81D – AUGUST 2020 – REVISED SEPTEMBER 2022

Changes from Revision C (June 2021) to Revision D (September 2022)	Page
• Changed from Restricted to Public.....	1
• Additional supply current when TSREF is enabled.....	12
• DVDD output voltage.....	12
• NEG5V pin voltage.....	12
• Diagnostic measurements resolution.....	12
• Added paragraph to Section 9.3.1.2	24
• Changed 380µs to 1.35ms in Section 9.3.1.6	25
• Added Note to Section 9.3.2.1.3.1	29
• Deleted as in the Main ADC path and added Note to Section 9.3.2.2.1.1	32
• Changed text in Section 9.3.2.3	36
• Changed latch to load in Section 9.3.3.3.3	42
• Added text to Section 9.3.5	48
• Changed above to below in Figure 9-23	49
• Changed Figure 9-25 to show response frame start time is after TX_HOLF_OFF expiration time.....	50
• Changed Read to Write in Table 9-9	52
• Changed Figure 9-27 to the correct bit-width per byte.....	54
• Added $t_{prog_otp} = 100ms$ and During this time no data communications is allowed to step 4 in Table 9-26	80
• Changed BQ79606-Q1 to BQ7961X-Q1 in Section 9.3.6.4	81
• Deleted Rcb resistor in Figure 9-56	100
• Multiple bits in Section 9.5.4.3.11 updated with Bit is self-cleared and added text.....	129
• Changed bit setting 11 to Reserved in Section 9.5.4.5.1	134
• Changed bit 6 to Reserved and changed bit 5 to 0 = Dynamic Alignment in Section 9.5.4.5.8	136
• Added sourced from TSREF regulator voltage and Default Reset setting 4% to COOLOFF[2:0] and sourced from TSREF regulator voltage and Default Reset setting 24% to OTCB_THR[3:0] in Section 9.5.4.7.5	155
• Added This bit is self clearing to BAL_TIME_GO in Section 9.5.4.7.8	156
• Added sourced from TSREF regulator voltage. and Default Reset setting 80% to UT_THR[2:0] and sourced from TSREF regulator voltage. and Default Reset setting 39% to OT_THR[4:0] in Section 9.5.4.8.5	158
• Updated correct register names in Section 9.5.4.13.3	170
• Added Note to UART_MIRROR_EN in Section 9.5.4.14.2	178
• Changed cap value to 2.2 µF and fixed isolator pinout in Figure 10-1	191
• Changed content, Figure 10-7 , Figure 10-8 , and Figure 10-9 in Section 10.2.1.2.7.2	197
• Changed cap values to 2.2 µF in Figure 10-11	200
• Changed Figure 12-3	206

The datasheet number will be changing.

Device Family	Change From:	Change To:
BQ79616-Q1, BQ79614-Q1, BQ79612-Q1	SLUSE81C	SLUSE81D

These changes may be reviewed at the datasheet links provided.

<http://www.ti.com/product/BQ79616-Q1>

Reason for Change:

To accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):			
No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.			
Changes to product identification resulting from this notification:			
None.			
Product Affected:			
BQ79612PAPRQ1	BQ79614PAPRQ1	BQ79616PAPRQ1	

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