

Product Change Notice (PCN)

Subject: Data Sheet Electrical Specification change for ISL21440* Product

Publication Date: 3/27/2017

Effective Date: 6/25/2017

Revision Description:

Initial Release

Description of Change:

This notice is to inform you that Intersil has updated the data sheet electrical specification on the Output Low Voltage (V_{OL}) maximum specification for both 3V and 5V supply conditions from “GND + 0.4” to “GND + 0.6” for the products listed below : -

Product List

ISL21440IRTZ

ISL21440IRTZ-T13

ISL21440IUZ

ISL21440IUZ-T13

Reason for Change:

The changes in the data sheet table align the data sheet with the product characteristics and are necessary to maintain product manufacturability in support of customer delivery requirements. Please refer Appendix A for details. The updated data sheet is available on the Intersil web site at :

<http://www.intersil.com/content/dam/intersil/documents/isl2/isl21440.pdf>

Impact on fit, form, function, quality & reliability:

The change will have no impact on the form, fit, function, quality, reliability and environmental compliance of the devices.

Product Identification:

There have been no changes to the die/silicon or product itself. There will be no change in the external marking of the packaged parts. Product affected by this change is identifiable via Intersil's internal traceability system.

Qualification status: Not Applicable

Sample availability: 3/27/2017

Device material declaration: Available upon request

Questions or requests pertaining to this change notice, including additional data or samples, must be sent to Intersil within 30 days of the publication date.

For additional information regarding this notice, please contact your regional change coordinator (below)			
Americas: PCN-US@INTERSIL.COM	Europe: PCN-EU@INTERSIL.COM	Japan: PCN-JP@INTERSIL.COM	Asia Pac: PCN-APAC@INTERSIL.COM

Appendix A : PA17014 Data Sheet Electrical Specification Updates

Output Low Voltage when output current (supply condition) at 1.8mA

From (page 5 of 14) :

Analog Specifications (V+ = +3.0V) V- = GND = 0V unless otherwise specified, T_A = +25°C. Boldface limits apply across the operating temperature range, -40°C to +125°C. (Continued)

SYMBOL	PARAMETER	TEST CONDITIONS		MIN (Note 11)	TYP (Note 10)	MAX (Note 11)	UNIT
PSRR	Power Supply Rejection Ratio	V+ = 2V to 11V	MSOP Package		0.25	1.1	mV/V
						1.2	mV/V
		TDFN Package			0.25	1.5	mV/V
							1.6
V _{HYST}	Hysteresis Input Voltage			REF - 50mV		REF	V
t _{PHL}	Propagation Delay - HIGH to LOW Transition	C _L = 100pF	Overdrive = 10mV		100		μs
			Overdrive = 100mV		50		μs
t _{PLH}	Propagation Delay - LOW to HIGH Transition	C _L = 100pF	Overdrive = 10mV		200		μs
			Overdrive = 100mV		100		μs
V _{OH}	Output High Voltage	I _O = -6mA		(V+) - 0.4			V
V _{OL}	Output Low Voltage	I _O = 1.8mA				GND + 0.4	V

To (page 5 of 15) : -

Analog Specifications (V+ = +3.0V) V- = GND = 0V unless otherwise specified, T_A = +25°C. Boldface limits apply across the operating temperature range, -40°C to +125°C. (Continued)

SYMBOL	PARAMETER	TEST CONDITIONS		MIN (Note 11)	TYP (Note 10)	MAX (Note 11)	UNIT
PSRR	Power Supply Rejection Ratio	V+ = 2V to 11V	MSOP Package		0.25	1.1	mV/V
						1.2	mV/V
		TDFN Package			0.25	1.5	mV/V
							1.6
V _{HYST}	Hysteresis Input Voltage			REF - 50mV		REF	V
t _{PHL}	Propagation Delay - HIGH to LOW Transition	C _L = 100pF	Overdrive = 10mV		100		μs
			Overdrive = 100mV		50		μs
t _{PLH}	Propagation Delay - LOW to HIGH Transition	C _L = 100pF	Overdrive = 10mV		200		μs
			Overdrive = 100mV		100		μs
V _{OH}	Output High Voltage	I _O = -6mA		(V+) - 0.4			V
V _{OL}	Output Low Voltage	I _O = 1.8mA				GND + 0.6	V

Appendix A : PA17014 Data Sheet Electrical Specification Updates

Output Low Voltage when output current (supply condition) at 3mA

From (page 4 of 14) :

Analog Specifications (V+ = +5.0V) V- = GND = 0V unless otherwise specified, T_A = +25°C. Boldface limits apply across the operating temperature range, -40°C to +125°C. (Continued)

SYMBOL	PARAMETER	TEST CONDITIONS		MIN (Note 11)	TYP (Note 10)	MAX (Note 11)	UNIT
PSRR	Power Supply Rejection Ratio	V+ = 2V to 11V	MSOP Package		0.25	1.1	mV/V
						1.2	mV/V
		TDFN Package			0.25	1.5	mV/V
							1.6
V _{HYST}	Hysteresis Input Voltage			REF - 50mV		REF	V
t _{PHL}	Propagation Delay - HIGH to LOW Transition	C _L = 100pF	Overdrive = 10mV		100		μs
			Overdrive = 100mV		50		μs
t _{PLH}	Propagation Delay - LOW to HIGH Transition	C _L = 100pF	Overdrive = 10mV		200		μs
			Overdrive = 100mV		100		μs
V _{OH}	Output High Voltage	I _O = -10mA		(V+) - 0.4			V
V _{OL}	Output Low Voltage	I _O = 3mA				GND + 0.4	V

To (page 4 of 15) : -

Analog Specifications (V+ = +5.0V) V- = GND = 0V unless otherwise specified, T_A = +25°C. Boldface limits apply across the operating temperature range, -40°C to +125°C. (Continued)

SYMBOL	PARAMETER	TEST CONDITIONS		MIN (Note 11)	TYP (Note 10)	MAX (Note 11)	UNIT
PSRR	Power Supply Rejection Ratio	V+ = 2V to 11V	MSOP Package		0.25	1.1	mV/V
						1.2	mV/V
		TDFN Package			0.25	1.5	mV/V
							1.6
V _{HYST}	Hysteresis Input Voltage			REF - 50mV		REF	V
t _{PHL}	Propagation Delay - HIGH to LOW Transition	C _L = 100pF	Overdrive = 10mV		100		μs
			Overdrive = 100mV		50		μs
t _{PLH}	Propagation Delay - LOW to HIGH Transition	C _L = 100pF	Overdrive = 10mV		200		μs
			Overdrive = 100mV		100		μs
V _{OH}	Output High Voltage	I _O = -10mA		(V+) - 0.4			V
V _{OL}	Output Low Voltage	I _O = 3mA				GND + 0.6	V