Title:	**Migrating from MX25L3255D to MX25L3255E, and Discontinuance of MX25L3255D**
Notification Number:	P-1207-0004
Notification Date:	August 1, 2012
Change Type:	Product Migration and Discontinuance

1. Change Identification

Product Identification	MX25L3255D
Reason of Change	Migration to new products and discontinuance of former products

2. Implementation Schedule

2-a. Affected Product Schedule

Last Order Date	April 30, 2013
Last Ship Date	July 31, 2013

2-b. Replacement Product Schedule

Sample Availability	Available

3. List of Product Affected

Affected Part Number	**Recommend Part Number**
MX25L3255DM2I-10G ➡	MX25L3255EM2I-10G
MX25L3255DXCI-10G	MX25L3255EXCI-10G

Should you have any questions or specific requirement for the extension of product supports, please contact your local Macronix sales representative within 30 days. For other information or specification of succeeding products, please contact your local Macronix sales representative or visit Macronix's Web site at http://www.macronix.com.

4. Supplementary

Additional Comments	
Reference Documents	Migration guide (AN174): Migrating from MX25L3255D to MX25L3255E

Major Differences:

Feature		MX25L3255E	MX25L3255D
Voltage		2.7~3.6 V	2.7~3.6 V
Interface		x1, x2, x4	x1, x2, x4
Package		24-TFBGA (6x8mm) 8-SOP (209mil)	24-TFBGA (6x8mm) 8-SOP (209mil)
Operation Temperature		-40~85°C	-40~85°C
Sector / Block		4KB / 32KB or 64KB	4KB / 64KB
Clock Rate (max.)	Normal Read	50MHz	33MHz
	1 I/O	104MHz	104MHz
	2 I/O	86MHz	75MHz
	4 I/O	104MHz	75MHz
Configurable Dummy Cycle		Yes, for 4READ mode only	No
CP Mode		Yes	Yes
Data Protection	Security OTP	4Kbits	4Kbits
	Block Protection	BP3~BP0	No
	T/B (Top/Bottom) Bit	Yes	No
	Single Block Protection	Yes, SRAM bit	Yes, NVM bit
	Permanent Lock Protection	Yes	Yes
	Permanent Lock Bit Lock-Down	Yes	No
	Read Lock Protection	Yes	Yes
SFDP		Yes	No
HOLD#		Yes	No

Comments:

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