



Customer Information Notification

202110025I : PCA9957 and PCA9959 LED Controller Data Sheet Update

Note: This notice is NXP Company Proprietary.

Issue Date: Dec 06, 2021 **Effective date:** Jan 10, 2022

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Change Category

Wafer Fab Process Assembly Process Product Marking Test Process Design
 Wafer Fab Materials Assembly Materials Mechanical Specification Test Equipment Errata
 Wafer Fab Location Assembly Location Packing/Shipping/Labeling Test Location Electrical spec./Test coverage
 Firmware Other: Data sheet update

PCN Overview

Description

On page 32 - Section 8.1 /CS paragraph 2nd sentence - the data sheet said "As long as /CS is HIGH, all slaves will not accept the clock signal or data, and the output SDO is in high-impedance state." This will be changed to "As long as /CS is HIGH, all slaves will not accept the clock signal or data, and output SDO is driven in low state, therefore SPI parallel connection is not supported."

Reason

Change data sheet to reflect actual operation of device which is SDO is held low when /CS is held high.

Identification of Affected Products

Product identification does not change

Anticipated Impact on Form, Fit, Function, Reliability or Quality

No Impact on form, fit, function, reliability or quality

Data Sheet Revision

A new datasheet will be issued

Disposition of Old Products

Existing inventory will be shipped until depleted

Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please contact NXP "Global Quality Support Team".

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

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